EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5	((margin OR extreme) ADJ test$3) AND computer AND (BMC OR (baseboard AJD management ADJ controller)) AND frequenc$3 AND ((digital ADJ parameter) WITH (frequency ADJ synthesizer)) AND (communicat$4 OR transmit$4 OR receiv $3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/14 16:24
L2	5	((margin OR extreme) ADJ test$3) AND computer AND (BMC OR (baseboard AJD management ADJ controller)) AND frequenc$3 AND ((digital ADJ parameter) AND (frequency ADJ synthesizer)) AND (communicat$4 OR transmit$4 OR receiv $3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/14 16:24
L3	5	((margin OR extreme) ADJ test$3) AND computer AND frequenc$3 AND ((digital ADJ parameter) AND (frequency ADJ synthesizer)) AND (communicat$4 OR transmit$4 OR receiv $3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/14 16:24

L4	7	((margin OR extreme) ADJ test$3) AND computer AND frequenc$3 AND ((digital ADJ parameter)) AND (communicat$4 OR transmit$4 OR receiv $3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/14 16:24
L5	302	((margin OR extreme) ADJ test$3) AND computer AND frequenc$3 AND (communicat$4 OR transmit$4 OR receiv $3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/14 16:24
L6	25	((margin OR extreme) ADJ test$3) AND computer AND frequenc$3 AND communicat$4 AND transmit$4 AND receiv $3 AND diagnos$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/14 16:24
L7	122	((margin OR extreme) ADJ test$3) AND computer AND frequenc$3 AND communicat$4 AND transmit$4 AND receiv $3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/14 16:24
L8	13	((frequenc$3 ADJ (margin OR extrem $3)) WITH test$3) AND server	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/14 16:24
L9	58	((frequenc$3 ADJ (margin OR extrem $3)) WITH test$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/14 16:24
L10	46	((integrated ADJ circuit) OR IC) AND amplif$3 AND switch $3 AND (analog ADJ multiplex$3) AND (serial ADJ communications ADJ interface) AND operation$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/14 16:24

L11	1	((integrated ADJ circuit) OR IC) AND amplif$3 AND (operational ADJ characteristic) AND switch AND (analog ADJ multiplex$3) AND (serial ADJ communications ADJ interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/14 16:24
L12	46	((integrated ADJ circuit) OR IC) AND amplif$3 AND switch $3 AND (analog ADJ multiplex$3) AND (serial ADJ communications ADJ interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/14 16:24
L13	45	((integrated ADJ circuit) OR IC) AND amplif$3 AND switch AND (analog ADJ multiplex$3) AND (serial ADJ communications ADJ interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/14 16:24
L14	4	(expander WITH generat$3 WITH bit WITH pattern) SAME control$4	US-PGPUB; USPAT; DERWENT	OR	ON	2008/09/14 16:24
L15	0	(expander WITH generat$3 WITH bit WITH pattern) SAME control$4 SAME synthesizer	US-PGPUB; USPAT; DERWENT	OR	ON	2008/09/14 16:24
L16	210	((margin OR extreme) ADJ test$3) AND computer AND frequenc$3 AND (communicat$4 OR transmit$4 OR receiv $3).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/14 16:24
L17	306	((margin OR extreme) ADJ test$3) AND control$4 AND frequenc$3 AND (communicat$4 OR transmit$4 OR receiv $3).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/14 16:24

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